

ABSTRACT

There are provided a radio-frequency amplifier and a radio-frequency wireless communication apparatus enabling reduction in insertion loss of a transistor and precise circuit design based on a measurement result.

An input-side line portion 3 and an output-side line portion 4 including an input slot line 30 and an output slot line 40 extending in parallel are formed on a substrate 1. In a connecting portion 20 of a transistor 2, a gate electrode G, a drain electrode D, and both source electrodes S are arranged in a coplanar manner. The gate electrode G, the drain electrode D, and the both source electrodes S are connected to DC electrodes 10 and 11 and a ground electrode 12, respectively, in a flip chip method via bumps 22, so that the orientation of the slot lines 30 and 40 is perpendicular to the orientation of arrangement of the gate electrode G and the drain electrode D. Preferably, the both source electrodes S of the transistor 2 are connected via an air bridge 21.